## महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

## **LESSON PLAN (SESSION 2025-26)**

CLASS:- B.C.A SEM:- IIIrd FACULTY NAME :- ANJALI CHAUDHARY

**SUBJECT :- Computer Organization and Architecture** 

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S.No.	Date.	Day	Paper No.	Unit Name.	Topic.
1	16-Jul-25	Wednesday	204N	UNIT 1: Digital Components	Overview of computer organization, logic gates
2	17-Jul-25	Thursday	204N	UNIT 1: Digital Components	Adders, flip-flops, encoders, decoders,
3	18-Jul-25	Friday	204N	UNIT 1: Digital Components	Adders, flip-flops, encoders, decoders,
4	19-Jul-25	Saturday	204N	UNIT 1: Digital Components	Adders, flip-flops, encoders, decoders,
5	21-Jul-25	Monday	204N	UNIT 1: Digital Components	Adders, flip-flops, encoders, decoders,
	22-Jul-25	Tuesday		CLASS TE	
	00 1 1 05	747 1 1	22.434		Multiplexers registers and shift
6	23-Jul-25	Wednesday	204N	UNIT 1: Digital Components	registers, counters, RAM, ROM
7	24-Jul-25	Thursday	204N	UNIT 1: Digital Components	Multiplexers, registers, and shift registers, counters, RAM, ROM
8	25-Jul-25	Friday	204N	UNIT 1: Digital Components	Multiplexers, registers, and shift registers, counters, RAM, ROM
9	26-Jul-25	Saturday	204N	UNIT 1: Digital Components	Multiplexers, registers, and shift registers, counters, RAM, ROM
10	28-Jul-25	Monday	204N	UNIT 1: Digital Components	Multiplexers, registers, and shift registers, counters, RAM, ROM
	29-Jul-25	Tuesday		MONTHY EV	
11	30-Jul-25	Wednesday	204N	UNIT 2: Data Representation and Computer Arithmetic	Number systems, ASCII Code,
12	31-Jul-25	Thursday	204N	UNIT 2: Data Representation and Computer Arithmetic	r's and (r-1)'s compliments
13	1-Aug-25	Friday	204N	UNIT 2: Data Representation and Computer Arithmetic	r's and (r-1)'s compliments
14	2-Aug-25	Saturday	204N	UNIT 2: Data Representation and Computer Arithmetic	Addition , subtraction, overflow
15	4-Aug-25	Monday	204N	UNIT 2: Data Representation and Computer Arithmetic	Addition , subtraction, overflow
	5-Aug-25	Tuesday		CLASS TE	ACHING
16	6-Aug-25	Wednesday	204N	UNIT 2: Data Representation and Computer Arithmetic	Addition , subtraction, overflow
17	7-Aug-25	Thursday	204N	UNIT 2: Data Representation and Computer Arithmetic	Addition and subtraction with signed magnitude data
18	8-Aug-25	Friday	204N	UNIT 2: Data Representation and Computer Arithmetic	Addition and subtraction with signed magnitude data

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19	11-Aug-25	Monday	204N	UNIT 2: Data Representation and Computer Arithmetic	Multiplication Algorithm, division Algorithm
20	12-Aug-25	Tuesday	204N	UNIT 2: Data Representation and Computer Arithmetic	Multiplication Algorithm, division Algorithm
	13-Aug-25	Wednesday		CLASS TE	
21	14 4 25	Tl	204N	UNIT 2: Data Representation	Multiplication Algorithm, division
21	14-Aug-25	Thursday	204N	and Computer Arithmetic	Algorithm
22	18-Aug-25	Monday	204N	UNIT 3: Register transfer and Micro operations	Bus and memory transfers
23	19-Aug-25	Tuesday	204N	UNIT 3: Register transfer and Micro operations	Bus and memory transfers
24	20-Aug-25	Wednesday	204N	UNIT 3: Register transfer and Micro operations	Three state bus buffers, Binary adder, Binary incremented
25	21-Aug-25	Thursday	204N	UNIT 3: Register transfer and	Three state bus buffers, Binary adder,
23	21-Aug-23	Thursday	204N	Micro operations	Binary incremented
	22-Aug-25	Friday	CLASS TEACHING		
26	23-Aug-25	Saturday	204N	UNIT 3: Register transfer and Micro operations	Three state bus buffers, Binary adder, Binary incremented
27	25-Aug-25	Monday	204N	UNIT 3: Register transfer and Micro operations	Three state bus buffers, Binary adder, Binary incremented
28	26-Aug-25	Tuesday	204N	UNIT 3: Register transfer and	Three state bus buffers, Binary adder,
				Micro operations UNIT 3: Register transfer and	Binary incremented  Arithmetic Circuit, Logic and shift
29	27-Aug-25	Wednesday	204N	Micro operations	micro operations, ALU
30	28-Aug-25	Thursday	204N	UNIT 3: Register transfer and	Arithmetic Circuit, Logic and shift
				Micro operations	micro operations, ALU
	29-Aug-25	Friday		MONTHY EV	
31	30-Aug-25	Saturday	204N	UNIT 3: Register transfer and Micro operations	Arithmetic Circuit, Logic and shift micro operations, ALU
32	1-Sep-25	Monday	204N	UNIT 3: Register transfer and Micro operations	Arithmetic Circuit, Logic and shift micro operations, ALU
33	2-Sep-25	Tuesday	204N	UNIT 4: Basic computer Organization:	Instruction Codes, Addressing modes,
34	3-Sep-25	Wednesday	204N	UNIT 4: Basic computer Organization:	Instruction Codes, Addressing modes,
35	4-Sep-25	Thursday	204N	UNIT 4: Basic computer Organization:	Timing and control signal, instruction cycle, memory reference instruction
	6-Sep-25	Saturday		CLASS TE	
				UNIT 4: Basic computer	Timing and control signal, instruction
36	8-Sep-25	Monday	204N	Organization:	cycle, memory reference instruction
37	9-Sep-25	Tuesday	204N	UNIT 4: Basic computer Organization:	Timing and control signal, instruction cycle, memory reference instruction
38	10-Sep-25	Wednesday	204N	UNIT 4: Basic computer Organization:	, , , , , , , , , , , , , , , , , , , ,
39	11-Sep-25	Thursday	204N	UNIT 4: Basic computer Organization:	Input output instruction, general register organization of CPU, memory stack

40	12-Sep-25	Friday	204N	UNIT 4: Basic computer Organization:	Input output instruction, general register organization of CPU, memory stack
	13-Sep-25	Saturday	CLASS TEACHING		
41	15-Sep-25	Monday	204N	UNIT 4: Basic computer Organization:	Input output instruction, general register organization of CPU, memory stack
42	16-Sep-25	Tuesday	204N	UNIT 4: Basic computer Organization:	Input output instruction, general register organization of CPU, memory stack
43	17-Sep-25	Wednesday	204N	UNIT 4: Basic computer Organization:	Input output instruction, general register organization of CPU, memory stack
44	18-Sep-25	Thursday	204N	UNIT 4: Basic computer Organization:	One address and two address instruction, software and hardware interrupts
45	19-Sep-25	Friday	204N	UNIT 4: Basic computer Organization:	One address and two address instruction, software and hardware interrupts
	20-Sep-25	Saturday	CLASS TEACHING		
46	22-Sep-25	Monday	204N	UNIT 4: Basic computer Organization:	One address and two address instruction, software and hardware interrupts
47	23-Sep-25	Tuesday	204N	UNIT 4: Basic computer Organization:	One address and two address instruction, software and hardware interrupts
48	24-Sep-25	Wednesday	204N	UNIT 4: Basic computer Organization:	One address and two address instruction, software and hardware interrupts
49	25-Sep-25	Thursday	204N	UNIT 4: Basic computer Organization:	One address and two address instruction, software and hardware interrupts
50	26-Sep-25	Friday	204N	UNIT 5: I/O and Memory organization	Input devices, synchronous and asynchronous communications
	27-Sep-25	Saturday		MONTHY EV	
			00.155	UNIT 5: I/O and Memory	Input devices, synchronous and
51	29-Sep-25	Monday	204N	organization	asynchronous communications
52	30-Sep-25	Tuesday	204N	UNIT 5: I/O and Memory organization	Input devices, synchronous and asynchronous communications
53	3-0ct-25	Friday	204N	UNIT 5: I/O and Memory	Input devices, synchronous and
	5 000 25	Tilday	20111	organization	asynchronous communications
54	4-0ct-25	Saturday	204N	UNIT 5: I/O and Memory organization	Input devices, synchronous and asynchronous communications
55	6-0ct-25	Monday	204N	UNIT 5: I/O and Memory organization	Input devices, synchronous and asynchronous communications
	7-Oct-25	Tuesday	CLASS TEACHING		

56	8-0ct-25	Wednesday	204N	UNIT 5: I/O and Memory	Modes of data transfer from I/O to	
	0 000 23	Weallesday	20111	organization	Memory	
57 9-0	9-0ct-25	Thursday	204N	UNIT 5: I/O and Memory	Modes of data transfer from I/O to	
	7 000 25		20411	organization	Memory	
58	10-Oct-25	Friday	204N	UNIT 5: I/O and Memory	Modes of data transfer from I/O to	
30	10-000-25	Tituay	204N	organization	Memory	
F0	11-0ct-25	Catuaday	204N	UNIT 5: I/O and Memory	DMA	
59	11-001-25	Saturday	204N	organization	DMA	
	13-0ct-25	Monday				
	14-0ct-25	Tuesday				
	15-0ct-25	Wednesday		MID - TERM EX	AM SCHEDIILE	
	16-0ct-25	Thursday		THE TERM EX	IIII SCHLDOLL	
	17-0ct-25	Friday				
	18-0ct-25	Saturday	<u> </u>			
60	21-0ct-25	Tuesday	204N	UNIT 5: I/O and Memory	DMA	
		,		organization		
61	24-0ct-25	Friday	204N	UNIT 5: I/O and Memory	DMA	
	21 000 20	Tiraay	_0	organization	5	
				UNIT 5: I/O and Memory	Memory hierarchy, Main memory,	
62	25-0ct-25	Saturday	204N	organization	Memory management, Cache	
				Organization	memory, virtual memory	
	27-Oct-25	Monday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory,	
63					Memory management, Cache	
					memory, virtual memory	
	28-Oct-25	Tuesday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory,	
64					Memory management, Cache	
					memory, virtual memory	
	29-0ct-25	Wednesday	CLASS TEACHING			
					Memory hierarchy, Main memory,	
65	30-Oct-25	Thursday	204N	UNIT 5: I/O and Memory organization	Memory management, Cache	
0.5					,	
					memory, virtual memory  Memory hierarchy, Main memory,	
66	21 Oct 25	Friday	204N	UNIT 5: I/O and Memory organization		
66	31-0ct-25				Memory management, Cache	
		+			memory, virtual memory	
	1-Nov-25	Saturday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory,	
67					Memory management, Cache	
					memory, virtual memory	
	3-Nov-25	Monday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory,	
68					Memory management, Cache	
					memory, virtual memory	
	4-Nov-25	Tuesday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory,	
69					Memory management, Cache	
					memory, virtual memory	
	6-Nov-25	Thursday		CLASS TE		
70	7-Nov-25	Friday	204N	Revision	Revision	
71	8-Nov-25	Saturday	204N	Revision	Revision	
72	10-Nov-25	Monday	204N	Revision	Revision	
73	11-Nov-25	Tuesday	204N 204N	Revision	Revision	
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74	12-Nov-25	Wednesday	204N	Revision	Revision
	13-Nov-25	Thursday	CLASS TEACHING		
75	14-Nov-25	Friday	204N	Revision	Revision
76	15-Nov-25	Saturday	204N	Revision	Revision