महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

LESSON PLAN (SESSION 2025-26)

CLASS:- B.C.A   SEM:- IIIrd   FACULTY NAME :- ANJALI CHAUDHARY

SUBJECT :- Computer Organization and Architecture

S.No.	Date.	Day	Paper No.	Unit Name.	Topic.
1	16-Jul-25	Wednesday	204N	UNIT 1: Digital Components	Overview of computer organization, logic gates
2	17-Jul-25	Thursday	204N	UNIT 1: Digital Components	Adders, flip-flops, encoders, decoders,
3	18-Jul-25	Friday	204N	UNIT 1: Digital Components	Adders, flip-flops, encoders, decoders,
4	19-Jul-25	Saturday	204N	UNIT 1: Digital Components	Adders, flip-flops, encoders, decoders,
5	21-Jul-25	Monday	204N	UNIT 1: Digital Components	Adders, flip-flops, encoders, decoders,
	22-Jul-25	Tuesday		CLASS TEACHING	
6	23-Jul-25	Wednesday	204N	UNIT 1: Digital Components	Multiplexers, registers, and shift registers, counters, RAM, ROM
7	24-Jul-25	Thursday	204N	UNIT 1: Digital Components	Multiplexers, registers, and shift registers, counters, RAM, ROM
8	25-Jul-25	Friday	204N	UNIT 1: Digital Components	Multiplexers, registers, and shift registers, counters, RAM, ROM
9	26-Jul-25	Saturday	204N	UNIT 1: Digital Components	Multiplexers, registers, and shift registers, counters, RAM, ROM
10	28-Jul-25	Monday	204N	UNIT 1: Digital Components	Multiplexers, registers, and shift registers, counters, RAM, ROM
	29-Jul-25	Tuesday		MONTHY EVALUATION	
11	30-Jul-25	Wednesday	204N	UNIT 2: Data Representation and Computer Arithmetic	Number systems, ASCII Code,
12	31-Jul-25	Thursday	204N	UNIT 2: Data Representation and Computer Arithmetic	r's and (r-1)'s compliments
13	1-Aug-25	Friday	204N	UNIT 2: Data Representation and Computer Arithmetic	r's and (r-1)'s compliments
14	2-Aug-25	Saturday	204N	UNIT 2: Data Representation and Computer Arithmetic	Addition , subtraction, overflow
15	4-Aug-25	Monday	204N	UNIT 2: Data Representation and Computer Arithmetic	Addition , subtraction, overflow
	5-Aug-25	Tuesday		CLASS TEACHING	
16	6-Aug-25	Wednesday	204N	UNIT 2: Data Representation and Computer Arithmetic	Addition , subtraction, overflow
17	7-Aug-25	Thursday	204N	UNIT 2: Data Representation and Computer Arithmetic	Addition and subtraction with signed magnitude data
18	8-Aug-25	Friday	204N	UNIT 2: Data Representation and Computer Arithmetic	Addition and subtraction with signed magnitude data

19	11-Aug-25	Monday	204N	UNIT 2: Data Representation and Computer Arithmetic	Multiplication Algorithm, division Algorithm
20	12-Aug-25	Tuesday	204N	UNIT 2: Data Representation and Computer Arithmetic	Multiplication Algorithm, division Algorithm
	13-Aug-25	Wednesday	**CLASS TEACHING**		
21	14-Aug-25	Thursday	204N	UNIT 2: Data Representation and Computer Arithmetic	Multiplication Algorithm, division Algorithm
22	18-Aug-25	Monday	204N	UNIT 3: Register transfer and Micro operations	Bus and memory transfers
23	19-Aug-25	Tuesday	204N	UNIT 3: Register transfer and Micro operations	Bus and memory transfers
24	20-Aug-25	Wednesday	204N	UNIT 3: Register transfer and Micro operations	Three state bus buffers, Binary adder, Binary incremented
25	21-Aug-25	Thursday	204N	UNIT 3: Register transfer and Micro operations	Three state bus buffers, Binary adder, Binary incremented
	22-Aug-25	Friday	**CLASS TEACHING**		
26	23-Aug-25	Saturday	204N	UNIT 3: Register transfer and Micro operations	Three state bus buffers, Binary adder, Binary incremented
27	25-Aug-25	Monday	204N	UNIT 3: Register transfer and Micro operations	Three state bus buffers, Binary adder, Binary incremented
28	26-Aug-25	Tuesday	204N	UNIT 3: Register transfer and Micro operations	Three state bus buffers, Binary adder, Binary incremented
29	27-Aug-25	Wednesday	204N	UNIT 3: Register transfer and Micro operations	Arithmetic Circuit, Logic and shift micro operations, ALU
30	28-Aug-25	Thursday	204N	UNIT 3: Register transfer and Micro operations	Arithmetic Circuit, Logic and shift micro operations, ALU
	29-Aug-25	Friday	**MONTHY EVALUATION**		
31	30-Aug-25	Saturday	204N	UNIT 3: Register transfer and Micro operations	Arithmetic Circuit, Logic and shift micro operations, ALU
32	1-Sep-25	Monday	204N	UNIT 3: Register transfer and Micro operations	Arithmetic Circuit, Logic and shift micro operations, ALU
33	2-Sep-25	Tuesday	204N	UNIT 4: Basic computer Organization:	Instruction Codes, Addressing modes,
34	3-Sep-25	Wednesday	204N	UNIT 4: Basic computer Organization:	Instruction Codes, Addressing modes,
35	4-Sep-25	Thursday	204N	UNIT 4: Basic computer Organization:	Timing and control signal, instruction cycle, memory reference instruction
	6-Sep-25	Saturday	**CLASS TEACHING**		
36	8-Sep-25	Monday	204N	UNIT 4: Basic computer Organization:	Timing and control signal, instruction cycle, memory reference instruction
37	9-Sep-25	Tuesday	204N	UNIT 4: Basic computer Organization:	Timing and control signal, instruction cycle, memory reference instruction
38	10-Sep-25	Wednesday	204N	UNIT 4: Basic computer Organization:	
39	11-Sep-25	Thursday	204N	UNIT 4: Basic computer Organization:	Input output instruction, general register organization of CPU, memory stack

40	12-Sep-25	Friday	204N	UNIT 4: Basic computer Organization:	Input output instruction, general register organization of CPU, memory stack
	13-Sep-25	Saturday	CLASS TEACHING		
41	15-Sep-25	Monday	204N	UNIT 4: Basic computer Organization:	Input output instruction, general register organization of CPU, memory stack
42	16-Sep-25	Tuesday	204N	UNIT 4: Basic computer Organization:	Input output instruction, general register organization of CPU, memory stack
43	17-Sep-25	Wednesday	204N	UNIT 4: Basic computer Organization:	Input output instruction, general register organization of CPU, memory stack
44	18-Sep-25	Thursday	204N	UNIT 4: Basic computer Organization:	One address and two address instruction, software and hardware interrupts
45	19-Sep-25	Friday	204N	UNIT 4: Basic computer Organization:	One address and two address instruction, software and hardware interrupts
	20-Sep-25	Saturday	CLASS TEACHING		
46	22-Sep-25	Monday	204N	UNIT 4: Basic computer Organization:	One address and two address instruction, software and hardware interrupts
47	23-Sep-25	Tuesday	204N	UNIT 4: Basic computer Organization:	One address and two address instruction, software and hardware interrupts
48	24-Sep-25	Wednesday	204N	UNIT 4: Basic computer Organization:	One address and two address instruction, software and hardware interrupts
49	25-Sep-25	Thursday	204N	UNIT 4: Basic computer Organization:	One address and two address instruction, software and hardware interrupts
50	26-Sep-25	Friday	204N	UNIT 5: I/O and Memory organization	Input devices, synchronous and asynchronous communications
	27-Sep-25	Saturday	MONTHY EVALUATION		
51	29-Sep-25	Monday	204N	UNIT 5: I/O and Memory organization	Input devices, synchronous and asynchronous communications
52	30-Sep-25	Tuesday	204N	UNIT 5: I/O and Memory organization	Input devices, synchronous and asynchronous communications
53	3-Oct-25	Friday	204N	UNIT 5: I/O and Memory organization	Input devices, synchronous and asynchronous communications
54	4-Oct-25	Saturday	204N	UNIT 5: I/O and Memory organization	Input devices, synchronous and asynchronous communications
55	6-Oct-25	Monday	204N	UNIT 5: I/O and Memory organization	Input devices, synchronous and asynchronous communications
	7-Oct-25	Tuesday	CLASS TEACHING		

56	8-Oct-25	Wednesday	204N	UNIT 5: I/O and Memory organization	Modes of data transfer from I/O to Memory
57	9-Oct-25	Thursday	204N	UNIT 5: I/O and Memory organization	Modes of data transfer from I/O to Memory
58	10-Oct-25	Friday	204N	UNIT 5: I/O and Memory organization	Modes of data transfer from I/O to Memory
59	11-Oct-25	Saturday	204N	UNIT 5: I/O and Memory organization	DMA
	13-Oct-25	Monday	**MID - TERM EXAM SCHEDULE**		
	14-Oct-25	Tuesday			
	15-Oct-25	Wednesday			
	16-Oct-25	Thursday			
	17-Oct-25	Friday			
	18-Oct-25	Saturday			
60	21-Oct-25	Tuesday	204N	UNIT 5: I/O and Memory organization	DMA
61	24-Oct-25	Friday	204N	UNIT 5: I/O and Memory organization	DMA
62	25-Oct-25	Saturday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory, Memory management, Cache memory, virtual memory
63	27-Oct-25	Monday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory, Memory management, Cache memory, virtual memory
64	28-Oct-25	Tuesday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory, Memory management, Cache memory, virtual memory
	29-Oct-25	Wednesday	**CLASS TEACHING**		
65	30-Oct-25	Thursday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory, Memory management, Cache memory, virtual memory
66	31-Oct-25	Friday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory, Memory management, Cache memory, virtual memory
67	1-Nov-25	Saturday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory, Memory management, Cache memory, virtual memory
68	3-Nov-25	Monday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory, Memory management, Cache memory, virtual memory
69	4-Nov-25	Tuesday	204N	UNIT 5: I/O and Memory organization	Memory hierarchy, Main memory, Memory management, Cache memory, virtual memory
	6-Nov-25	Thursday	**CLASS TEACHING**		
70	7-Nov-25	Friday	204N	Revision	Revision
71	8-Nov-25	Saturday	204N	Revision	Revision
72	10-Nov-25	Monday	204N	Revision	Revision
73	11-Nov-25	Tuesday	204N	Revision	Revision

74	12-Nov-25	Wednesday	204N	Revision	Revision
	13-Nov-25	Thursday	**CLASS TEACHING**		
75	14-Nov-25	Friday	204N	Revision	Revision
76	15-Nov-25	Saturday	204N	Revision	Revision